

IN THE UNITED STATES RECEIVING OFFICE

Applicant : Bernd Deutschmann, et al.

Serial No. : 10/588,984

Filed : August 10, 2006

Title : CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING AN INTEGRATED SEMICONDUCTOR CIRCUIT

MAIL STOP PCT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUBMISSION OF MISSING PARTS OF APPLICATION

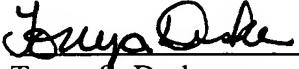
This application was filed without a declaration signed by the inventor. A Notification of Missing Requirements was never issued. In order to complete this application, applicant as a large entity submits herewith the following:

- Payment of the surcharge of \$130 for late filing of the basic filing fee and/or declaration.
- A check in the total amount of \$130 is attached.
- A Combined Declaration and Power of Attorney in compliance with 37 CFR §§1.63 and 1.497;

It is understood that this perfects the application and no additional papers or filing fees are required. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: November 21, 2006



Tonya S. Drake
Reg. No. 57,861

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110
Telephone: (617) 542-5070
Facsimile: (617) 542-8906
21486934.doc

11/27/2006 ATRAN1 0000030 10588984

01 FC:1617

130.00 OP

CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. EL 980029637 US

November 21, 2006

Date of Deposit